

FORM PTO-1449 (SUBSTITUTE)

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEINFORMATION DISCLOSURE
STATEMENT BY APPLICANT
(37 CFR 1.98(b))Attorney Docket No.: P2001,0099
Appl. No.

Applicant GUNNAR KRAUSE ET AL.

Filing Date February 14, 2002
Group Art Unitc971 U.S. PRO
10/07/5529
02/14/02

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
aw	A	5,971,923	10/26/99	Finger			
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						

FOREIGN PATENT DOCUMENT

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES NO
	J						
	K						
	L						
	M						
	N						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

aw		Ikeda, H.: "High-Speed DRAM Architecture Development", IEEE, Vol. 34, No. 5, May 1999, pp. 685-692
aw		"DDR SDRAM Functionality and Controller Read Data Capture", Micron Technology Inc., Vol. 8, Issue 3, pp. 1-24

EXAMINER	DATE CONSIDERED
aw	1-26-05
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	